

SYSTEM AND METHOD FOR SECURING AN INTEGRATED CIRCUIT AS AGAINST SUBSEQUENT REPROGRAMMING
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1/2

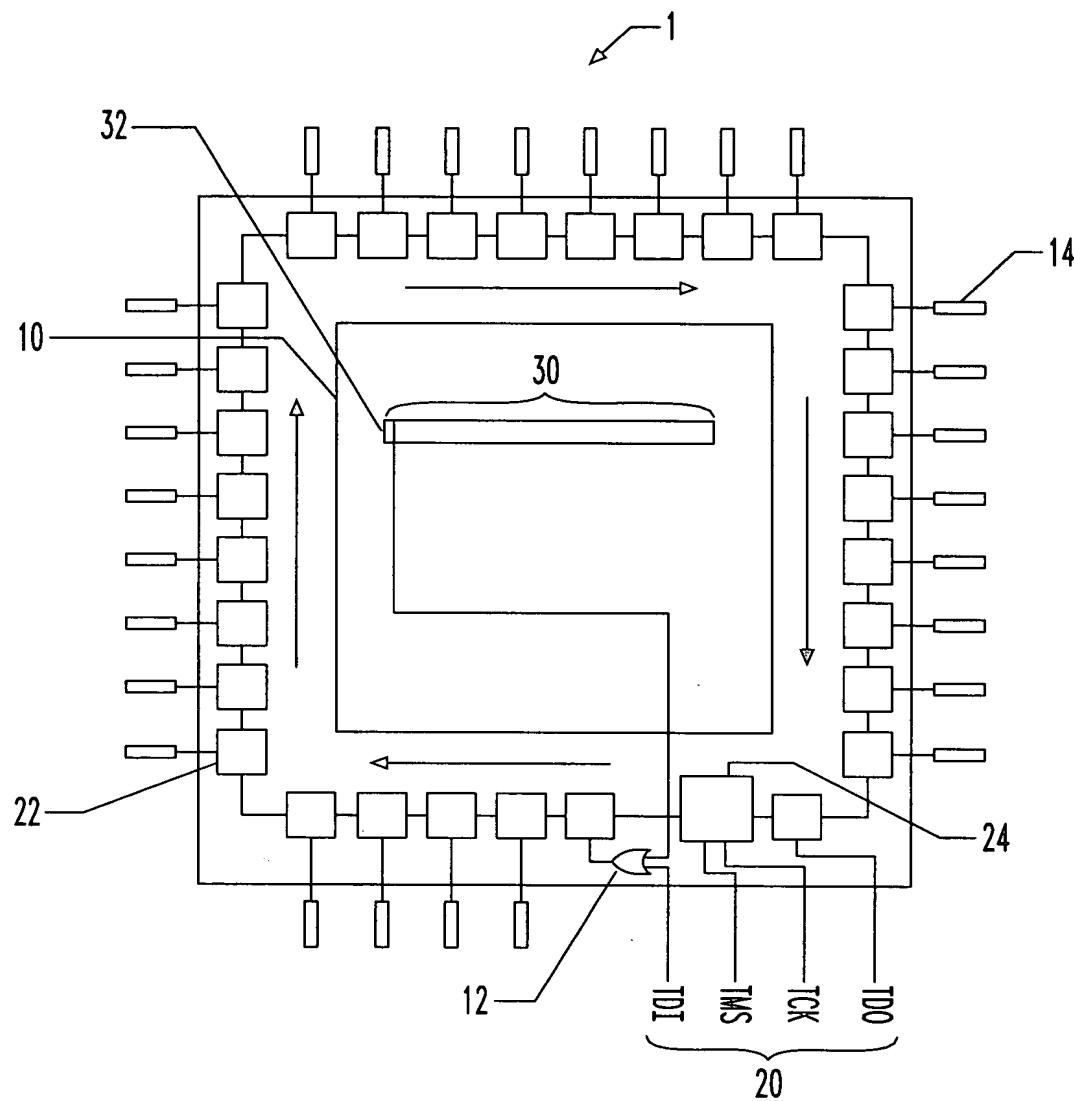


FIG. 1

2/2

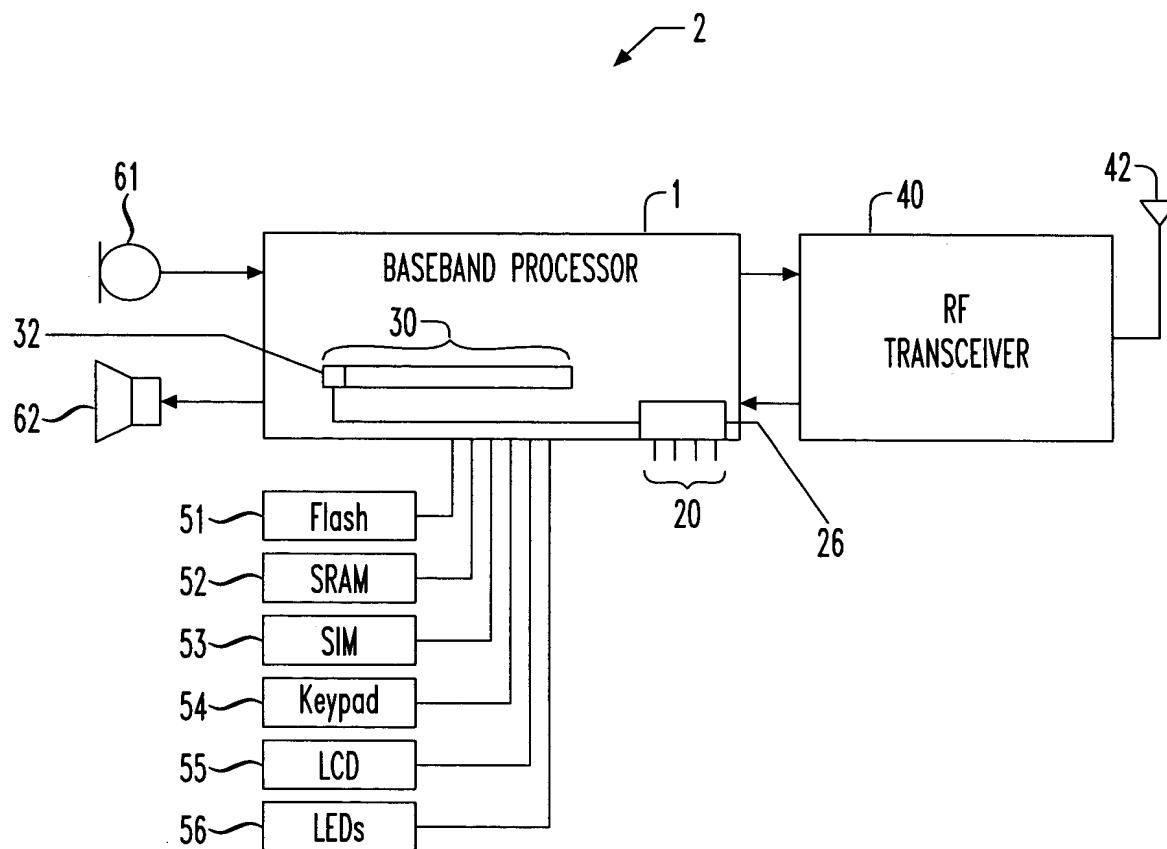


FIG. 2